

FMLR 61-STM

High performance sub-GHz LoRaWAN[®] IoT module

FMLR sub-GHz low power wireless LoRa[®] / LoRaWAN[®] module with STM32L0 and optional flash memory



Description

FMLR-61-STM is a LoRa® and LoRaWAN® loT module that provides wireless connectivity to devices and sensors in the sub-GHz spectrum. With a frequency coverage from 150 MHz to 960 MHz, it supports all major sub-GHz ISM bands around the world. In addition to the LoRa® modulation scheme, the module supports (G)FSK modulation.

Due to its low power consumption, the module is ideal for applications with small-sized batteries. The integrated low power 32-bit ARM Cortex[®]-M0+ microcontroller featuring 192 kB flash and 20 kB RAM offers sufficient resources to run advanced user applications.

Features

- Semtech SX1261 based
- LoRaWAN[®] IoT module
- Line-of-sight range of up to 100 km
- ARM Cortex[®]-M0+ MCU
- Optional ext. flash, TCXO, U.FL connector
- STM32L0 MCU for stack and user application
- ▶ Tiny FMLR footprint: 14 × 19.5 mm

Applications

- Asset tracking
- Health care
- Industry 4.0
- Smart agriculture
- Smart building
- Smart city
- Smart metering
- Smart retail
- Supply chain and logistics



Document Information

About

File name	Document type	Date	Revision
DS-FMLR-61-STM	Datasheet	2023/05/10	2.0

Revision History

Date	Release	Changes
2021/03/21	1.0	Initial revision
2023/04/11	2.0	Fully revised
2023/05/10	2.1	Added difference L0/L4 variants

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Functional Description

The **FMLR-61-STM** LoRa[®] and LoRaWAN[®] loT module provides wireless connectivity to devices, systems and sensors communicating with low data rates over a long distance. The module supports a frequency range from 150 MHz to 960 MHz. Due to its low power consumption, the module is ideal for devices running on small-sized batteries. The integrated ARM Cortex[®]-M0+ 32-bit microcontroller is capable of running entire RF stacks and has sufficient resources to run user applications.

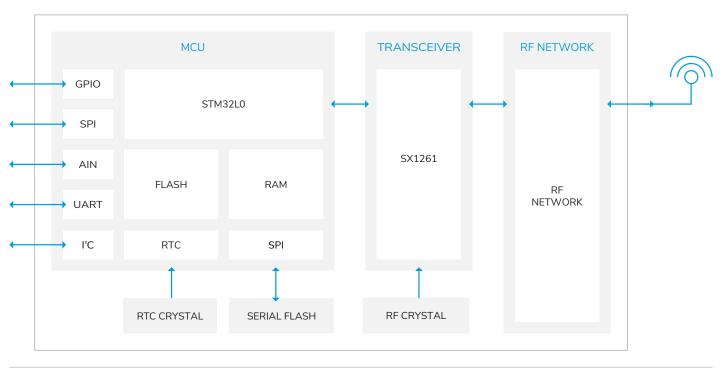


Figure 1: Block diagram FMLR-61-STM

The module is available with additional on-board flash memory to support Over-the-Air (OTA) update and additional data storage. In addition to the LoRa[®] modulation scheme, the module supports (G)FSK modulation, thus enabling communication with standards like Wireless M-Bus and IEEE802.15.4g.

To support fast prototyping and development, the firmware, including the wireless stack, can be updated via SWD or UART bootloader.



Technical Specifications

Core Components

LoRa [®] transceiver	Semtech SX1261
Microcontroller	STM STM32L071RZH6
Core	Cortex [®] -M0+, 32 MHz
Flash memory	192 kB
RAM	20 kB
EEPROM	6 kB
Ext. flash, optional (-4M)	Macronix MX25R4035FZUIL0, 512 kB

Mechanical Specifications

Weight	2 g
Dimensions	14 × 19.5 × 2 mm

Operating Conditions

Temperature	-20 – 85 °C
Humidity	0 – 95 % RH, non-condensing

Absolute Maximum Ratings

Parameter	Min	Max	Unit
Ext. supply voltage on all power pins ($V_{\text{\tiny DD}}$)	-0.3	3.6	V
Input voltage on any pin	$V_{ss} - 0.3$	V_{dd}	V
DC current on any pin		15	mA
Storage temperature	-40	+85	°C

▲ WARNING!

Stressing the device beyond the «Absolute Maximum Ratings» may cause permanent damage.



Operating Conditions

Parameter	Min	Тур	Max	Unit
Standard operating voltage ($V_{\text{\tiny DD}}$)	1.8		3.5	V
Digital IO pin input low voltage	V _{ss}		$0.3 \cdot V_{\text{DD}}$	V
Digital IO pin input high voltage	$0.7 \cdot V_{dD}$		V _{dd}	V
Digital IO pin output low voltage	0		0.4	V
Digital IO pin output high voltage	$V_{\text{DD}} - 0.4$		V _{dd}	V
Current consumption, TX mode (+10dBm), 3.3V ¹		18		mA
Current consumption, TX mode (+14dBm), 3.3V ¹		25.5		mA
Current consumption, RX mode, LoRa® 125 kHz ¹		10.1		mA
Current consumption, sleep mode		1.2		μA
Highest receiver sensitivity ¹			-148	dBm
RF output power ¹			14	dBm

¹See transceiver datasheet for detailed specifications

Certifications

CE		
UKCA		
LoRaWAN [®] certification	pending	
FCC	pending	

On-Board LED

The on-board LED is connected to GPIO PB8. Actively drive port to low (0V) to light up LED. Drive port high or high Z to disable LED.

Differences between L0 and L4 variants

Due to differences in STM32 MCU pin out, on modules using STM32L4 devcies, port PC3 is connected to VDD. Port PC3 should always be kept in its default configuration (input with no pull up or pull down). Other pin configuration might lead to device damage or excessive current consumption!

On modules using STM32L0 port PC3 is not available as an external pin of the MCU package and pin configuration has no effect. It's recommended to keep pin in default configuration.



Module Pinout GND ANT GND 43 42 41 GND 37 GND 1 6x–STI PB0 NRST 36 2 Rev. C PC13 PA0 35 3 PB1 SDA 4 34 33 U2TX SCL 5 32 U2RX BOOT0 6 U1RX 31 PA5 7 U1TX 8 30 MOSI SWCLK 9 29 MISO SWDIO 28 SCK 10 PA12 11 27 PB2 PA11 26 PA8 12 25 GND VIN 13 24 PC1 PC7 14 15 16 17 18 19 20 23 PC10 PA15 PC12 PC6 PC11 PD2 PB7 РНО PH1

Figure 2: Module Pinout

#	Pad name	MCU pad	Description		# Pad name	# Pad name MCU pad
	GND		Ground (V _{ss})		21 PB7	21 PB7 PB7
	NRST	NRST	MCU Reset		22 PH0	22 PH0 PH0
3	PC13	PC13	GPIO	2	3 PH1	3 PH1 PH1
1	SDA	PB9	I ² C1, GPIO	24	PC1	PC1 PC1
5	SCL	PB6	I ² C1, GPIO	25	GND	GND
	BOOT0	BOOT0	MCU BOOT0	26	5 PA8	5 PA8 PA8
	U1RX	PA10	UART1 RX	27	PB2	PB2 PB2
	U1TX	PA9	UART1 TX	28	3 SCK ¹	B SCK ¹ PB3
	SWCLK	PA14	DBG Clock / GPIO	29	MISO ¹	MISO ¹ PB4
) SWDIO	PA13	DBG Data / GPIO	30	MOSI ¹	MOSI ¹ PB5
1	PA12	PA12	USB P ² / GPIO	31	PA5	. PA5 PA5
2	2 PA11	PA11	USB N ² / GPIO	32	U2RX	U2RX PA3
	8 VIN		Supply Voltage $V_{\rm DD}$	33	U2TX	U2TX PA2
	PC7	PC7	GPIO	34	PB1	PB1 PB1
15	5 PC6	PC6	GPIO	35	PA0	PA0 PA0
16	5 PC10	PC10	GPIO	36	PB0	PB0 PB0
17	' PC11	PC11	GPIO	37	GND	GND
18	8 PA15	PA15	GPIO	41	GND	GND
19	PD2	PD2	GPIO	42	ANT	ANT
20) PC12	PC12	GPIO	43	GND	GND

¹ If the module variant contains an external flash, these pins are connected internally and should not be used as GPIO pins!
² USB not available on all variants



FMLR Family Footprint

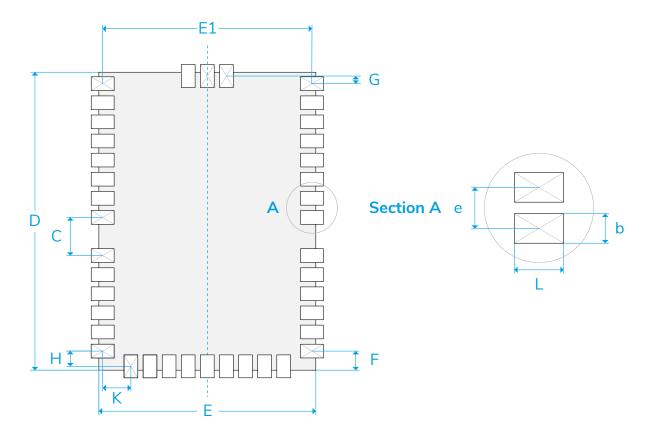


Figure 3: FMLR Module Footprint

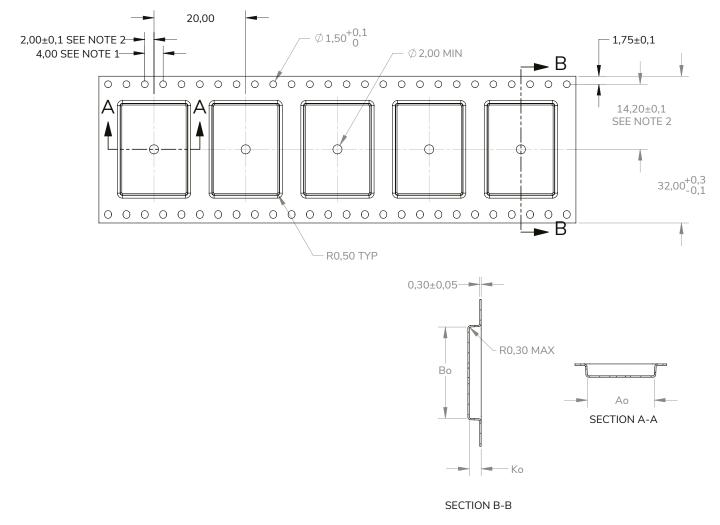
FMLR Footprint Dimensions*

Dimension (see Figure 3: FMLR Module Footprint)	Min	Тур	Max
b	0.85	0.9	0.95
C		2.5	
D		19.5	
E		14.2	
E1		13.7	
e		1.25	
F		1.25	
G		0.5	
Н		1	
K		1.85	
L	1.45	1.5	1.55

*All dimensions in mm



Tape Information



	DIM	±
Ao³	14,60	0.1
Bo ³	19,90	0.1
Ko	2,60	0.1

 $^{\rm 1}$ 10 Sprocket Hole Pitch Cumulative Tolerance ± 0.2

² Pocket Position Relative To Sprocket Hole Measured As True Position Of Pocket, Not Pocket Hole

 $^{\scriptscriptstyle 3}$ Ao And Bo Are Measured On A Plane At A Distance "R" Above The Bottom Of The Pocket.

All dimensions in mm

Tolerances unless – specified 1 PL ± 0.2

2 PL ± 0.10



Recommended Soldering Conditions

The following graph shows a typical temperature profile for the module soldering process. The exact values to be used in production is highly depending on other parameters of the soldering process, such as solde-ring paste, PCB design, soldering process, etc.

Reflow process should be finished within 2 cycles.

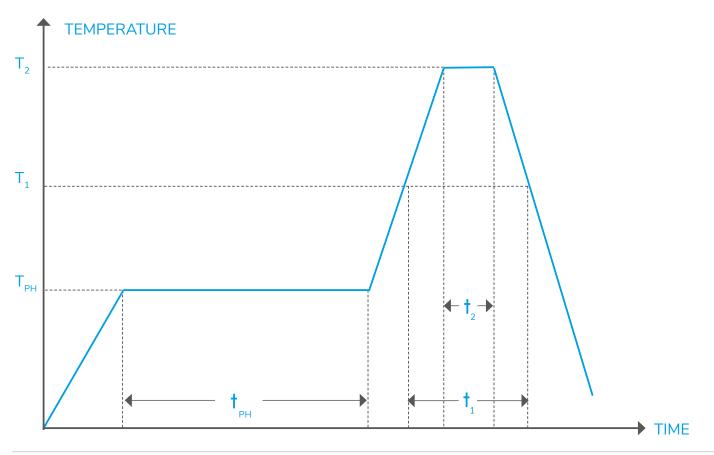


Figure 4: Soldering Profile

Soldering Conditions

Step (see Figure: Soldering Profile)	Temperature	Time
Preheat (T _{PH} , t _{PH})	150 to 180 °C	120 s
Heating (T ₁ , t ₁)	220 °C	60 s
Reflow (T ₂ , t ₂)	255 °C	5 s



Additional Documentation

Additional Resources

Product information page	https://miromico.ch/fmlr-61-stm
Technical documentation	https://docs.miromico.ch/modules/

Device Options

Product ID	MCU options				RF	
	Cortex [®] -M0+	192KB flash	20KB RAM	4Mbit Flash	U.FL connect.	Antenna pad
FMLR-61-U-STL0Z	~	~	~		~	
FMLR-61-P-STL0Z	~	~	~			~
FMLR-61-U-STL0Z-4M	~	~	~	~	~	
FMLR-61-P-STL0Z-4M	~	~	~	~		~

Options for other STM32 variants (USB, Cortex[®]-M0+/M4 with FPU, etc.) and external flash sizes are available on request.



Keep in Touch

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